

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: )  
RUMENNIK, V., et al. ) Examiner: Unassigned J. McInerney  
Serial No.: Not Yet Assigned ) Art Unit: Unassigned 418/02  
Filing Date: September 20, 2001 )  
(Concurrently Herewith) )  
For: HIGH-VOLTAGE TRANSISTOR WITH )  
MULTI-LAYER CONDUCTION REGION )  

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Divisional Application of: )  
Serial No. 09/574,563 )  
Filed: May 17, 2000 )  
Examiner: Hu, S. )  
Art Unit: 2811 )

**Preliminary Amendment**

Commissioner of Patents and Trademarks  
Box Patent Application  
Washington, D.C. 20231

Sir:

Prior to examination of the above-identified divisional application, please amend the application as follows:

**In The Specification:**

Page 1, paragraph starting on line 5, should be replaced with:

The present application is a divisional application of Serial No. 09/574,563, filed May 17, 2000, which is a divisional application of Serial No. 09/245,030, filed